

## (c) BJT BIASING AND STABILIZATION

- The dc load line of transistor circuit
  - is a graph between  $I_C$  and  $V_{CE}$
  - is a graph between  $I_C$  and  $I_B$
  - does not contain the Q Point
  - is a curved line
- The ac load line of a transistor circuit is steeper than its dc line because
  - ac signal sees less load resistance
  - $I_C$  is higher
  - input signal varies in magnitude
  - none of the above
- The maximum peak to peak output voltage swing is obtained when Q point of a circuit is located
  - near saturation point
  - near cutoff point
  - at the center of the load line
  - atleast on the load line
- The positive part of the output signal in a transistor circuit starts clipping, if Q point of the circuit moves
  - toward the saturation point
  - toward the cutoff point
  - toward the center of the load line
  - none of the above
- The negative part of the output signal in a transistor circuit starts clipping if the Q point of the circuit moves
  - toward the saturation point
  - toward the cutoff point
  - toward the center of the load line
  - none of the above
- The Q-point in a voltage amplifier is selected in the middle of the active region because
  - it gives better stability
  - the circuit needs a small
  - the biasing circuit then need less number of resistors
  - it gives distortions less output
- The biasing circuit that gives best stability to Q point is
  - base resistance biasing
  - feedback resistor biasing

(c) potential divider biasing

(d) emitter resistor biasing

8. The potential divider biasing is used in amplifiers to

(a) limit the input ac signal going to the base

(b) reduce dc base current

(c) reduce the cost of the circuit by limiting the number of resistors

(d) make the operating point almost independent of  $\beta$

9. The ideal value of stability factor of a biasing circuit is

(a) 1

(c) 10

(b) 5

(d) 100

10. The universal bias stabilization circuit is the most popular because

(a)  $I_C$  does not depend on transistor characteristic

(b) its  $\beta$  sensitivity is low

(c) voltage divider is heavily loaded by transistor base

(d)  $I_C$  equals to  $I_E$

11. Improper biasing of the transistor circuit leads to

(a) excessive heat production at collector terminal

(b) distortion in output signal

(c) faulty location of load line

(d) heavy loading of emitter terminal

12. The voltage divider biasing circuit is used in amplifiers quite often because it

(a) limits the ac signal going to base

(b) makes the operating point almost independent of  $\beta$

(c) reduces the dc base current

(d) reduces the cost of the circuit

13. For a transistor amplifier with self- biasing network, the following components are used:

$R_1 = 4K\Omega$ ,  $R_2 = 4K\Omega$  and  $R_E = 1K\Omega$ , the approximate value of stability factor will be

(a) 4

(c) 2

(b) 3

(d) 1.5

14. Which of the following components are used for bias compensation in transistor circuit
- (a) resistors
  - (b) rectifier diodes
  - (c) thermistors
  - (d) both (b) and (c) above

## Answers

- |         |         |         |         |
|---------|---------|---------|---------|
| 1. (a)  | 2. (a)  | 3. (c)  | 4. (b)  |
| 5. (a)  | 6. (d)  | 7. (c)  | 8. (d)  |
| 9. (a)  | 10. (b) | 11. (b) | 12. (b) |
| 13. (b) | 14. (d) |         |         |

GATESTUDY.COM

GATESTUDY.COM